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First Named Inventor	Leonard Forbes
Serial No.	10/612,725
Filing Date	July 1, 2003
Group Art Unit	2815
Examiner Name	Jesse A. Fenty
Confirmation No.	5767
Attorney Docket No.	400.258US01
Title: APPARATUS AND METHOD FOR SPLIT TRANSISTOR MEMORY HAVING IMPROVED ENDURANCE	

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Mail Stop: AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with 37 C.F.R. §§ 1.56 and 1.97, *et seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified Application.

Pursuant to 37 C.F.R. 1.98 (a)(2)(i), as this application was filed after June 30, 2003, Applicant has not included copies of U.S. Patents or U.S. Patent Applications. Applicant respectfully requests that this Supplemental Information Disclosure Statement be entered and the references listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to MPEP §609, Applicant further requests that the Examiner initial next to each reference on the Form 1449 to indicate that the listed references have been considered. Applicant further requests that a copy of the initialed Form 1449 be returned with the next official communication.

The enclosed references were recently cited in a communication in a counterpart foreign application. A copy of the International Search Report is enclosed herewith.

As an Office Action has issued in this application, Applicant encloses a check in the amount of \$180.00. However, the Commissioner for Patents is hereby authorized to charge any additional fees to Deposit Account No. 501373.

If the Examiner has any questions or concerns regarding this application, please contact the undersigned at (612) 312-2207.

03/28/2005 GWORDOF1 00000024 10612725

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180.00 OP

Date: 3/22/05

Respectfully submitted,



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		Sheet 1 of 1

U.S. Patent References				
Examiner Initials	Document No.	Issue/Publication Date	Name	Filing Date
	4,558,344	12/10/1985	Perlegos	01/29/1982
	5,973,352	10/26/1999	Noble	08/20/1997
	6,143,636	11/7/2000	Forbes	08/20/1998
	6,377,070 B1	04/23/2002	Forbes	02/09/2001

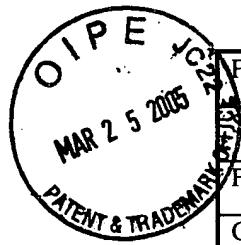
Foreign Patent References				
Examiner Initials	Foreign Patent		Name	Publication Date
	Country	No.		
	EP	0 485 018 A2	Philips Electronics	05/13/1992

Other References	
Examiner Initials	Author, Title, Date, Pages, etc.

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Examiner Signature		Date Considered	
*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			

Based on Form PTO-FB-A820 Patent and Trademark Office, U.S. Department of Commerce



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**GENERAL
TRANSMITTAL
FORM UNDER 37 CFR 1.8
(LARGE ENTITY)**

Title: APPARATUS AND METHOD FOR SPLIT TRANSISTOR MEMORY HAVING IMPROVED ENDURANCE

Mail Stop: AMENDMENT
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Enclosures

The following documents are enclosed:

- Supplemental Information Disclosure Statement (1 pg.); Form 1449 (1 pg.); 1 copy of cited reference; U.S. reference(s) not included pursuant to 37 C.F.R. 1.98 (c)(2)(i);
- A check in the amount of \$180.00 (IDS);
- An itemized return-receipt postcard

Leffert Jay & Polglaze, P.A.
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**Please charge any additional fees or credit any overpayments
to Deposit Account No. 501373.**

CUSTOMER NO. 27073

Submitted By

Name	Andrew C. Walseth	Reg. No.	43,234	Telephone	(612) 312-2207
Signature				Date	3/22/05

Certificate of Mailing

I certify that this correspondence and the identified documents listed on this transmittal are being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Mail Stop: AMENDMENT, Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450 on March 22, 2005.

Name	Rhonda L. Foley	Signature	
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(LARGE ENTITY TRANSMITTAL UNDER 37 CFR § 1.8)